AZ DISPLAYS, INC.

COMPLETE LCD SOLUTIONS

SPECIFICATIONS FOR LIQUID CRYSTAL DISPLAY

PART NUMBER: DATE:

AGM1212N SERIES APRIL 04, 2007

1. FUNCTIONS

Glass Thickness : 1.1mm

Viewing Direction :6 O'clock

Driving Scheme : 1/128Duty, 1/12 Bias

Power Supply for logic : 5.0V

Backli ghtCol or : Whi te

Display Content : 128*128 Dots

 V_{LCD} : 18.5V

Operation Temperature :-20 to $+70^{\circ}$ C

Storage temperature : -25 to +75 $^{\circ}$ C

Controller IC : T6 963C

Driv er IC : \$6B0 086

2. MODULE

Module Size : 106(L)*9 2(W)*14. 1(H) mm

Viewing Area : 73(L)m m*73 (W)m m

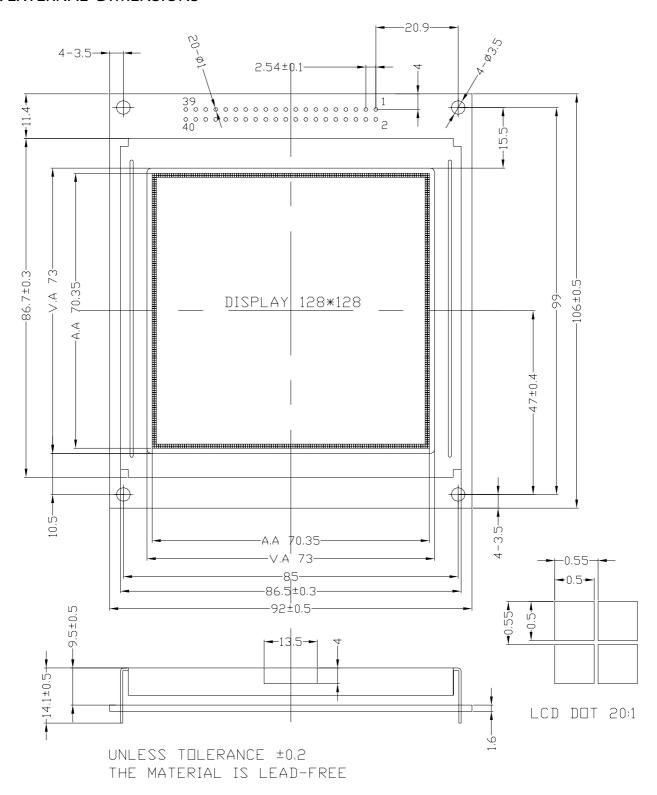
Active Area : 70.35(L)m m*70.35 (W)m m

Dot Pitch : 0.55 (W)m m*0.55 (H)mm

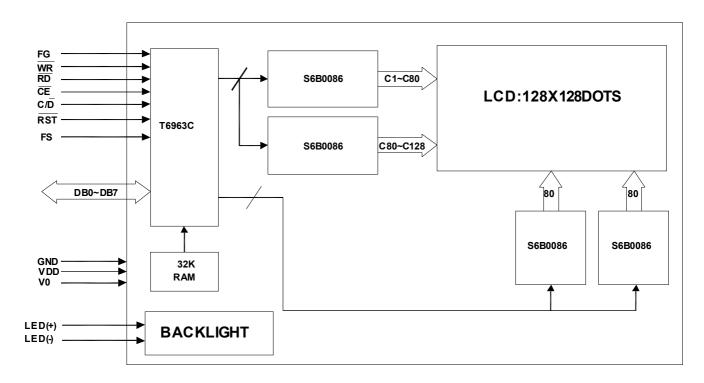
Dot Size : 0.50 (W)mm*0.50 (H)mm

Dot Gap : 0.05 mm

3. EXTERNAL DIMENSIONS



4. BLOCK DIAGRAM



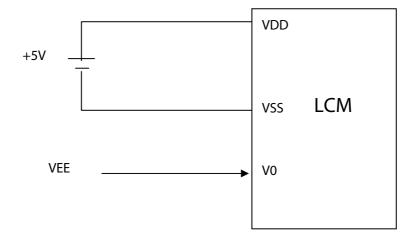
5. PIN ASSIGNMENT

NO.	SYMBOL	FUNCTION				
1 FC	•	Frame ground				
2 /0	SCAN	Active LOW: Force column scan counter to run continuously in order to obtain a complete cycle				
3 V	SS	Ground				
4,6,8,10	D S00~DS03	4 bits to indicate the column being scanned range0 to 15 corresponding to COL1 to COL16, with DS00 as LSB. Outputis latched at the most recently touched position. When read while "TOUCH" is positive, the touched column is identified.*				
5	VDD	Power supply for logic(+5V)				
7 V(Power supply for LCD drive				
9	/WR	Write Command or data to module when "L"				
11	/RD	Read Command or data from module when "L"				
12,14,16	D S04~DS06	3bits to indicate the row being scanned range 0 to 7 corresponding to ROW1 to ROW8, with DS4 as LSB & DS6 as MSB. Output is latched at the most recently touched position. When read while "TOUCH" is positive, the touched column is identified.*				
13	/CE	Enable LCD controller when "L"				
15	C//D	Command/data select. "H" for command read/write. "L" for data read/write				
17 /	RST	LCD Con troller reset. Controller initialize and DB00-DB07 are set to be high impedance when /RST is "L"				
18 //	SCA N	Active LOW: Standby to scan. Scanning will begin when a touch is identified at any of the sensing position, and will continue until touch is removed.				

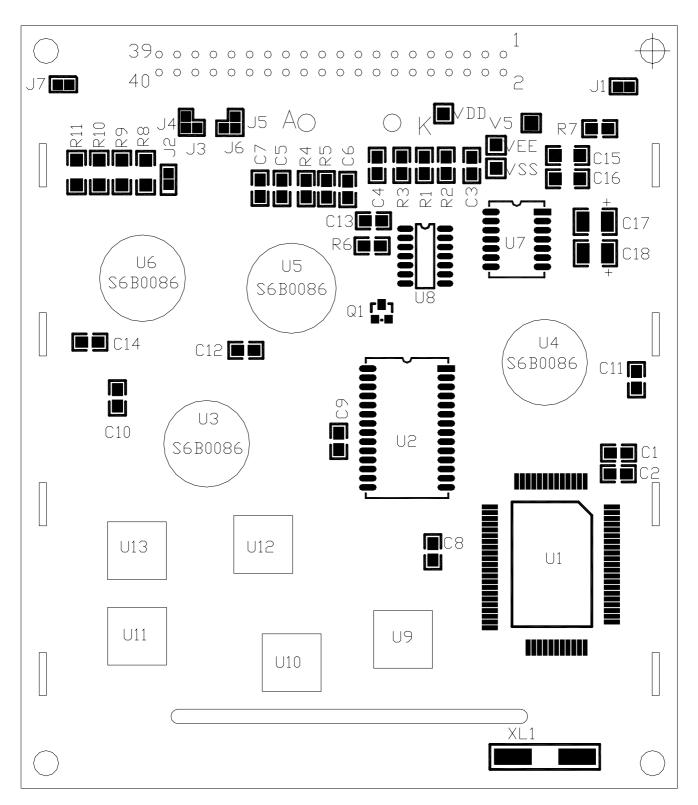
19,21,23,25, 27,29,31,33,	DB0 ~DB7	LCD data input/output. DB0(pin10) is LS B and DB7(pin17) is MSB.
20 E	N D	A 4ms positive pulse generated at the end of a complete scan cycle (Max cycle time: 64ms)
22 T	OUCH	A 2ms positive pulse when scanning reaches an identified touch position. It can be used as an interrupt.
24,26,28,30,3 2,34,36,38,40	NC. No	Connection
35	FS	Font select. "H" for 6x 8 font & "L" for 8x8 font
37 LI	D +(A)	Please refer to item 8.1 PCB drawing and description
39 LI	ED-(K)	Thease refer to item of the drawing and description

^{*} If D S00 to DS06 is read at the "END, then the last touched position will be identified.

6. POWER SUPPLY



7.1 PCB DRAWING AND DESCRIPTION



Note: It is only a draft drawing to show the components on the PCB. We should update the drawing after the PCB sample is approved.

DESCRIPTION:

7-1-1. The polarity of the pin 37 and the pin 39:

	symbol J3,J5 J6, J4		LED Polarity		
symbol	state	,00,	J 4	37 Pin	39 Pin
J6,J4	Each solder-bridge	Each closed	Each open	A node	Cathode
J3,J5	Each solder-bridge	Each open	Each closed	Cathode	Anode

Note: In application module, J3=J5 =J2=closed, J4=J6= open.

7-1-2. The J1 is metal-bezel GND to module GND and J7 is mountingholes GND to module GND.

Note: In application module, J1= J7=closed

7-1-3.T he LED resistor should be bridged when J2 is closed

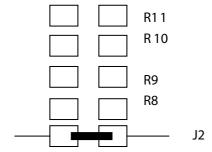
Note: In application module, J2=closed

7-1-4.The R8 and the R9, R10, R11 are the LED resist or.

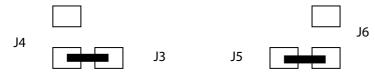
Note: In application module, R8 = R9= R10= R11=open

7.2 Example application

7-2-1. The LE D resistor should be bridged as following.



7-2-2. The 37 pin is the anode and the 39 pin is the cathodeas following.



7-2-3.The 37 pin is the cathodeand the 39 pin is the anodeas following.



7-2-4. The metal-bezel is on ground as following.



8. ABSOLUTE MAXIMUM RATINGS(Vss=0V, $Ta=25^{\circ}$ C)

PARAMETER	SYMBOL	RA TING	UNIT
Supply Voltage (Logic)	V_{DD}	-0.3 to 7.0	V
Input voltage	V _{IN}	-0.3 to VDD +0.3	V
Operating Temperature	Topr	-20 to +70	$^{\circ}$
StorageTemperature	Tstg	-25 to +75	${\mathbb C}$

9. ELECTRICAL CHARACTERISTICS

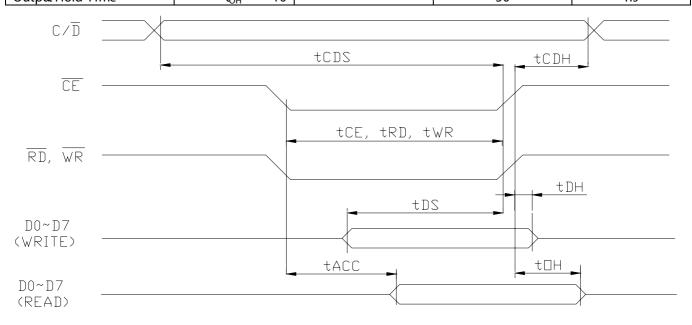
1). DC Characteristics

Ta=25°C, VSS=0V

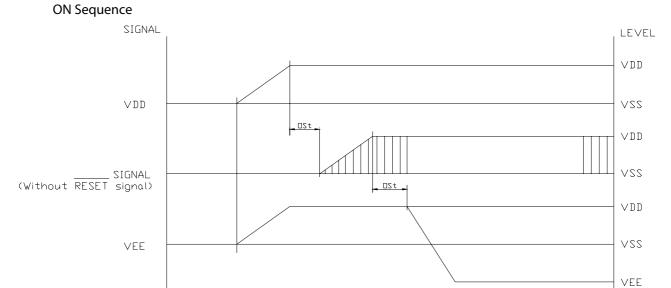
Parameter Symbo	l	Conditions	Min.	Тур.	Max.	Units
Supply Voltage (Logic)	V_{DD} - V_{SS}	-	4.5	5.0	5.5	V
High Level In put Voltage	V _{IH}	V _{DD} =5.0V±10%	V _{DD} -2.2	-	V_{DD}	V
Low Level I nputVoltage	V _{IL}	V _{DD} =5.0V ±10%	0	-	0.8	V
High Level Output Voltage	V _{OH}	$I_{OH}=0.75$ mA	V _{DD} -0.3	-	V_{DD}	V
Low Level Output Voltage	V _{OL}	$I_{OL}=0.75$ mA	0	-	0.3	V
Current	I _{DD(1)}	V _{DD} =5.0V	- 3	3.3	6.0	mA
Consumption(Operating)		$f_{OSC} = 3.0 MHz$				
Current Consumption (Halt)	I _{DD(2)}	V _{DD} =5.0V			3	uA

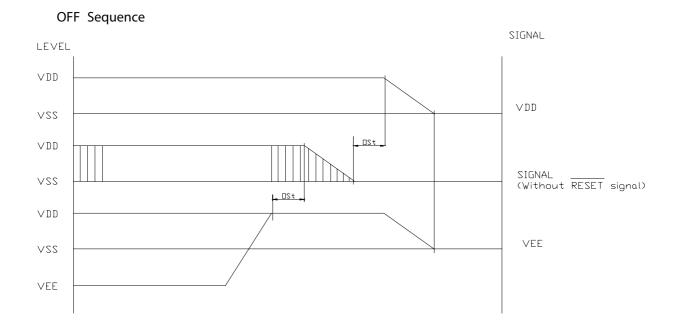
2). AC Characteristics

Parameter Sym	ool		Min.	Max.	Units
C/D Setup Time	t _{CDS}	10	0	-	ns
C/D Hold Time	t _{CDH}	10		-	ns
CE, RD, W R Pulse Width	$t_{CE,}, t_{RD}, t_{WR}$		80	-	ns
Data Setup Time	t _{DS}	80		-	ns
Data Hold Time	t _{DH}	40		-	ns
Access Time	t _{ACC}	-		150	ns
Output Hold Time	tон	10		50	ns

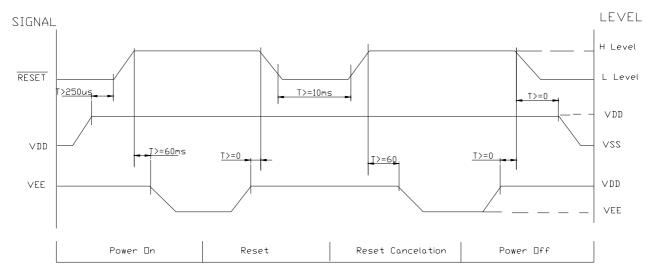


3). Power Supply ON/OFF Sequence





Reset Sequence



Please maintain the above sequence when turning on and off the power supply of the module.

If VEE is supplied to the module while internal alternate signal for LCD driving (M) is unstable or RESET is active, DC component will be supplied to the LCD panel. This may cause damage to the LCD module.

10. BACKLIGHT ELECTRICAL/ O PTICAL SPECIFICATIONS

10.1 A bsolute Maxi mum R atings (T a=25 ♥

Item Symbol		Conditions	Rating	Unit
A bsolute Maximum Forward Current	lfm		75	mA
Peak Forward Current	lfp	1 Msec Plus 10% Duty Cy cle	180	mA
R everse Voltage	Vr		1	V
Power Dissipation	Pd		225	mW

10.2 Backlight Electr o/Optical Characteristics

Item Symbol		Min.	Тур.	Max.	Unit	Condifion
Forward Voltage	Vf	2.9	3.2 3.5		V	lf=45mA
Reverse Current	lr		30		uA	Vr=0.8V
Peak Wave Length	λр				nm	
Spectral Li ne Half Width	Δλ				nm	
Luminance Lv			TBD		cd/m²	lf=45mA
BacklightColor	White					

10.3 Backlight Drawing

