AZ DISPLAYS

SPECIFICATIONS FOR LIQUID CRYSTAL DISPLAY

| CUSTOMER APPROVAL | | | | | |
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| ※ PART NO. : <u>A</u> | QM1264HA-FLW-FB | W(AZ DISPLAY | <u>(S) VER1.0</u> | | |
| APPROVAL | | COMPANY CHOP | | | |
| CUSTOMER | | | | | |
| COMMENTS | | | | | |

| AZ DISPLAYS ENGINEERING APPROVAL | | | | | | |
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| DESIGNED BY CHECKED BY APPROVED BY | | | | | | |
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1.0 GENERAL SPECS

| 1. Display Format | 128*64Dots |
|---------------------------|---------------------------------|
| 2. Power Supply | 3.3V |
| 3. Overall Module Size | 55.0mm(W) x 46.0mm(H) x5.4mm(D) |
| 4. Viewing Area(W*H) | 51.0mm(W) x 31.0mm(H) |
| 5. Dot Size (W*H) | 0.36mm(W) x 0.42mm(H) |
| 6. Dot Pitch (W*H) | 0.375mm(W) x 0.435mm(H) |
| 7. Viewing Direction | 6:00 O'Clock |
| 8. Driving Method | 1/65Duty,1/9Bias |
| 9. Controller IC | ST7565R |
| 10. Display Mode | FSTN/Positive/Transflecitve |
| 11. Backlight Options | White LED/Side |
| 12. Operating temperature | -20°C ~ 70°C |
| 13. Storage temperature | -30°C ~ 80°C |
| 14. RoHS | RoHS compliant |

2.0 ABSOLUTE MAXIMUM RATINGS

| Item | Symbol | Min | Тур | Max | Unit |
|--------------------------------|----------|---------|-----|---------|------|
| Operating temperature | Тор | -20 | - | 70 | °C |
| Storage temperature | Tst | -30 | - | 80 | °C |
| Input voltage | Vin | Vss-0.3 | | Vdd+0.3 | V |
| Supply voltage for logic | Vdd- Vss | -0.3 | - | 3.6 | V |
| Supply voltage for LCD driving | V0-Vss | 4.0 | - | 12.0 | V |

3.0 ELECTRICAL CHARACTERISTICS

3.1 Electrical Characteristics Of LCM

| Item | Symbol | Condition | Min | Тур | Max | Unit |
|--------------------------------|--------|-----------------------|--------|-----|--------|------|
| Power Supply Voltage | Vdd | 25°C | | 3.3 | | V |
| Power Supply Current | ldd | Vdd=3.3V | | | 1.5 | mA |
| Input voltage (high) | Vih | Pins:(RS,/WR,/RD,/RST | 0.8Vdd | | Vdd | V |
| Input voltage (low) | Vil | `,/CS,D0-D7), | Vss | | 0.2Vdd | V |
| | | -20°C | | | | |
| Recommended LC Driving Voltage | V0-Vss | 25°C | 9.0 | 9.2 | 9.4 | V |
| | | 70°C | | | | |

3.2 The Characteristics Of LED Backlight

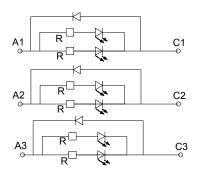
3.2.1 Electrical-Optical Characteristics Of LED Backlight (Ta=25°C)

| Item | Symbol | Condition | Min | Тур | Max | Unit |
|----------------------------|--------|----------------|------|-----|------|-------|
| Forward Current | lf | Vf=3.6V | | 60 | 90 | MA |
| Reverse Voltage | Vr | - | | | 5 | V |
| Luminance ^(2,3) | Lv | Vf=3.6V | 550 | 650 | | cd/m² |
| Uniformity | Δ | (Lvmin/Lvmax)% | 70% | | | - |
| Peak wave length | λр | - | | | | nm |
| Chroma coordinate | x | Vf=3.6V | 0.26 | | 0.32 | um |
| Oni onia coordinate | у | Vf=3.6V | 0.27 | | 0.32 | um |

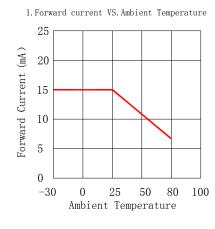
NOTE:

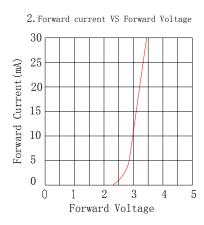
- (1)Luminance means the backlight brightness without LCD.
- (2)The luminance is the average value of 5 points, The measurement instrument is BM-7 luminance colorimeter. The diameter of aperture is Φ 5mm,

3.2.2 Backlight Control Circuit FOR LCM (3x2=6 pcs LED)



3.2.3 LED Characteristics Curves (for single led)

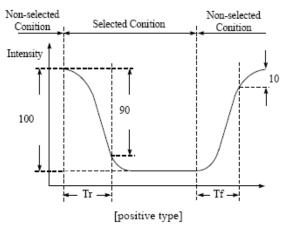


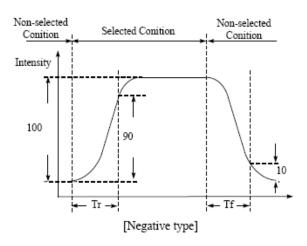


4.0 OPTICAL CHARACTERISTICS (Ta=25°C)

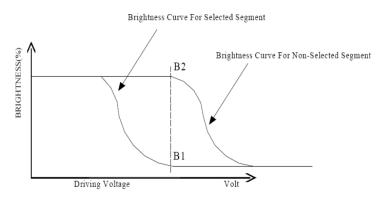
| Item | Symbol | Condition | Min | Тур | Max | Unit |
|------------------------------|--------|--------------|-----|-----|-----|------|
| Viewing angle (Left - right) | θ2 | Cr ≥ 2.0 | -35 | - | 35 | deg |
| Viewing angle (Up-down) | θ1 | Cr ≥ 2.0 | -25 | - | 40 | deg |
| Contrast Ratio | Cr | θ1=0°, θ2=0° | - | 3 | - | |
| Response time (rise) | Tr | θ1=0°, θ2=0° | - | 180 | 300 | ms |
| Response time (fall) | Tf | θ1=0°, θ2=0° | - | 150 | 250 | ms |

(1). Definition of Optical Response Time



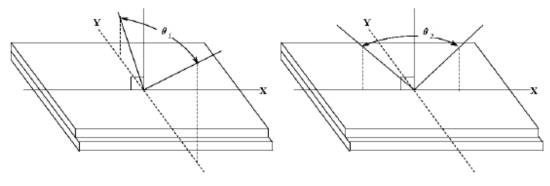


(2). Definition of Contrast Ratio



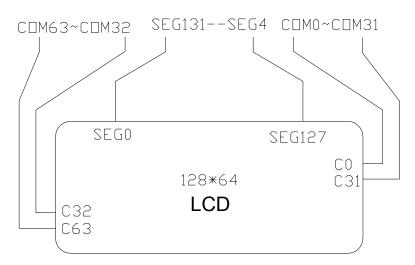
Cr= Brightness of Non-selected Segment(B2)
Brightness of selected Segment(B1)

(3). Definition of Viewing Angle θ 2 and θ 1



5.0 BLOCK DIAGRAM

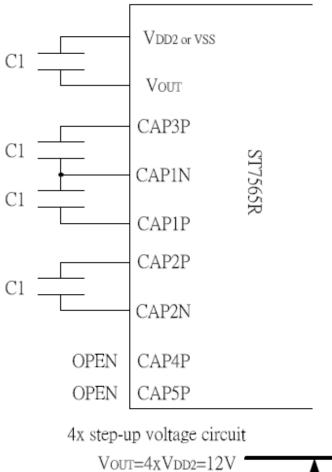
ST7565R

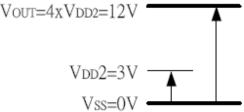


6.0 PIN ASSIGNMENT

| Pin No. | Symbol | Function |
|---------|-------------------------|---|
| 1-3 | A1,A2,A3 | LED backlighe Anodo(3.6V) |
| 18-20 | C1,C2,C3 | LED backlighe Cathode(0V) |
| 21-25 | V0~V4 | LCD driver supplies voltages |
| 26 | VSS | Power ground |
| 27-31 | CAP2N, CAP2P, CAP1P, | Capacitor 2-/2+/1+/1-/3+ pad for internal DC/DC voltage converter |
| | CAP1N, CAP3P | |
| 32 | VOUT | DC/DC voltage converter output |
| 33 | VSS | Power ground |
| 34 | VDD2 | Power Supply Booster |
| 35 | VDD | Power supply for logic(+3.3V) |
| 36 | SI | SPI data input |
| 37 | SCL | SPI Clock input |
| 38 | A0 | "H":display data; "L":control data |
| 39 | /RES | "L" active ,reset signal |
| 40 | CS1 | Chip select signal, "L" active |

7.0 POWER SUPPLY

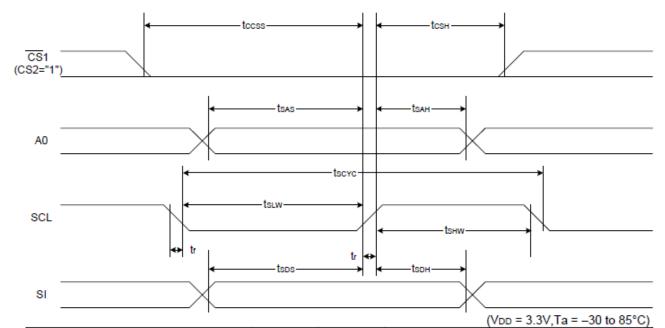




4x step-up voltage relationships

8.0 TIMING CHARACTERISTICS

ST7565R The 4-line SPI Interface Characteristics:



| Item | Signal | Symbol | Condition | Rating | | Units |
|-------------------------|--------|--------|-----------|--------|------|-------|
| item | Signai | Symbol | Condition | Min. | Max. | Units |
| 4-line SPI Clock Period | | Tscyc | | 50 | _ | |
| SCL "H" pulse width | - I | Tshw | | 25 | _ |] |
| SCL "L" pulse width | | Tslw | | 25 | _ | |
| Address setup time | A0 | Tsas | | 20 | _ |] |
| Address hold time | AU | Tsah | | 10 | _ | ns |
| Data setup time | CI | Tsds | | 20 | _ | |
| Data hold time | SI | Тѕон | | 10 | _ | 1 |
| CS-SCL time | 00 | Toss | | 20 | _ |] |
| CS-SCL time | CS | Tosh | | 40 | _ | |

| | | | | $(V_{DD} = 2.7V)$ | Ta = -30 to | 85°C) |
|-------------------------|--------|--------|-----------|-------------------|--|-------|
| Item | Signal | Symbol | Condition | Rat | ing | Units |
| item | Signai | Symbol | Condition | Min. | Max. — — — — — — — — — — — — — — — — — — — | Omits |
| 4-line SPI Clock Period | | Tscyc | | 100 | _ | |
| SCL "H" pulse width | SCL | Tshw | | 50 | _ | |
| SCL "L" pulse width | | Tslw | | 50 | _ | |
| Address setup time | - A0 | Tsas | | 30 | _ | |
| Address hold time | AU | Тѕан | | 20 | _ | ns |
| Data setup time | - SI | Tsps | | 30 | _ | |
| Data hold time | 31 | Тѕон | | 20 | _ | |
| CS-SCL time | - CS | Tcss | | 30 | _ | |
| CS-SCL time | _ US | Тсѕн | | 60 | _ | |

System Bus Read/Write Characteristics 1 (For the 8080 Series MPU)

READ Output disable time

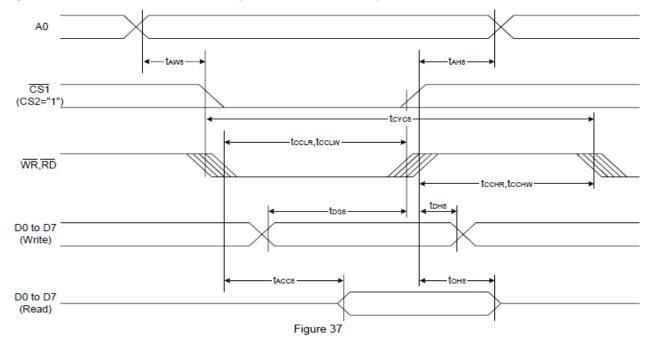


Table 24

 $(V_{DD} = 3.3V, Ta = -30 \text{ to } 85^{\circ}C)$ Rating Units Signal Symbol Condition Item Min. Max. Address hold time taн8 0 Address setup time A0 taws 0 System cycle time tcycs 240 Enable L pulse width (WRITE) 80 tccLw WR Enable H pulse width (WRITE) tсснw 80 Enable L pulse width (READ) 140 Ns tcclr RD Enable H pulse width (READ) tcchr 80 WRITE Data setup time 40 tos8 WRITE Address hold time t_{DH8} 0 D0 to D7 READ access time t_{ACC8} CL = 100 pF 70

 $C_L = 100 pF$

5

50

| | | | | $V_{DD} = 2.7V,$ | Ta = -30 to | 85°C) |
|------------------------------|------------|--------|-------------------------|------------------|-------------|-------|
| Item | Signal | Symbol | Condition | Rat | | Units |
| Kem | Signai | Symbol | Condition | Min. | Max. | Omics |
| Address hold time | | tah8 | | 0 | _ | |
| Address setup time | A0 | taws | | 0 | _ | |
| System cycle time |] | tcycs | | 400 | _ | |
| Enable L pulse width (WRITE) | WR | tccLw | | 220 | _ | |
| Enable H pulse width (WRITE) | WK | tсснw | | 180 | _ | |
| Enable L pulse width (READ) | RD | tocur | | 220 | _ | ns |
| Enable H pulse width (READ) | , KD | tcchr | | 180 | _ | |
| WRITE Data setup time | | toss | | 40 | _ |] |
| WRITE Address hold time | D0 to D7 | tонв | | 0 | _ | |
| READ access time | יום פו פום | taccs | C _L = 100 pF | _ | 140 | |
| READ Output disable time | | tонв | CL = 100 pF | 10 | 100 | |

toн8

System Bus Read/Write Characteristics 2 (For the 6800 Series MPU)

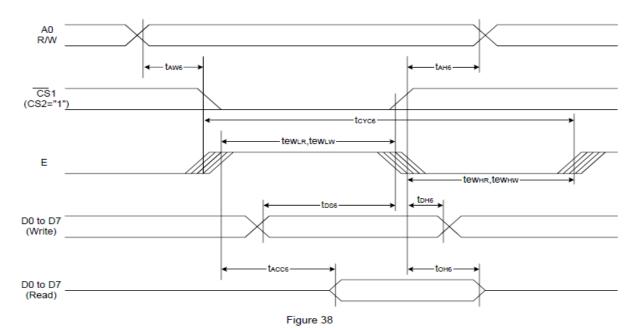


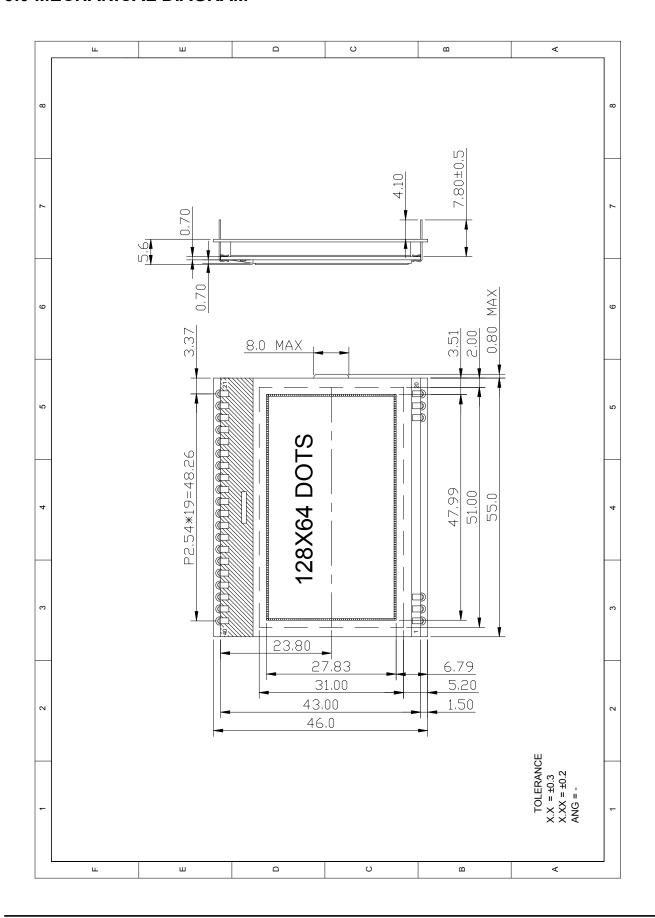
Table 26

 $(V_{DD} = 3.3V, Ta = -30 \text{ to } 85^{\circ}C)$.3∨, .__ Rating Max. Signal Symbol Condition Units Min. Address hold time tанв 0 0 Address setup time A0 tawe 240 tcyce System cycle time Enable L pulse width (WRITE) 80 tewLw WR Enable H pulse width (WRITE) tewnw 80 Enable L pulse width (READ) 80 tewcr ns RD Enable H pulse width (READ) 140 tewnr WRITE Data setup time tose 40 WRITE Address hold time 0 tонв D0 to D7 READ access time tacce $C_L = 100 pF$ 70 READ Output disable time tонв CL = 100 pF 5 50

| $(V_{DD} = 2.7V.Ta = -30 \text{ to } 8$ | 5°C) |
|---|------|

| | 6: | Ch.a.l | Canalistan | Rat | Units | | |
|------------------------------|----------|------------------|-------------|------|-------|-------|--|
| Item | Signal | Symbol Condition | | Min. | Max. | Units | |
| Address hold time | | tанв | | 0 | _ | | |
| Address setup time | A0 | taw6 | | 0 | _ | | |
| System cycle time | | tcyce | | 400 | _ | | |
| Enable L pulse width (WRITE) | WR | tewsw | | 220 | _ |] | |
| Enable H pulse width (WRITE) | WK | tеwнw | | 180 | _ | | |
| Enable L pulse width (READ) | RD | tewlr | | 220 | _ | ns | |
| Enable H pulse width (READ) | RD | tewnr | | 180 | _ |] | |
| WRITE Data setup time | | tose | | 40 | _ |] | |
| WRITE Address hold time | D0 to D7 | tонв | | 0 | _ |] | |
| READ access time | וט וט טו | tacce | CL = 100 pF | _ | 140 |] | |
| READ Output disable time | | tонв | CL = 100 pF | 10 | 100 | | |

9.0 MECHANICAL DIAGRAM



10.0 RELIABILITY TEST

| NO | Te | st Item | Description | Test Condition | Remark |
|----|---------------------------------|--|--|---|----------------|
| 1 | | High temperature storage | Applying the high storage temperature Under normal humidity for a long time Check normal performance | 80 °C 96hrs | |
| 2 | | Low temperature storage | Applying the low storage temperature Under normal humidity for a long time Check normal performance | -30°C 96hrs | |
| 3 | | High temperature Operation | Apply the electric stress(Voltage and current) Under high temperature for a long time | 70 °C 96hrs | Note1 |
| 4 | Environmental Test | Low temperature Operation | Apply the electric stress Under low temperature for a long time | -20°C 96hrs | Note1 Note2 |
| 5 | rest | High temperature/High Humidity Storage | Apply high temperature and high humidity storage for a long time | 90% RH 40 ° C 96hrs | Note2 |
| 6 | Temperature Cycle | | Apply the low and high temperature cycle -30°C<>25°C<>80°C<>25°C 30min 10min 30min 10min 1 cycle Check normal performance | -30°C/80°C 10 cycle | |
| 7 | Mechanical Test | Vibration test(Package state) | Applying vibration to product check normal performance | Freq:10~55~10H z Amplitude:0.75m m 1cycle time:1min X.Y.Z every direction for 15 cycles | |
| 8 | Shock test(package state) | | Applying shock to product check normal performance | Drop them through 70cm height to strike horizontal plane | |
| 9 | Other | | | · | |

Remark

Note1:Normal operations condition (25°C±5°C). Note2:Pay attention to keep dewdrops from the module during this test.

11.0 APPEARANCE CRITERIA

| Item | Description | Picture | Specification | | | | MA | MI | Inspection Method | | |
|--------------------------|--|---|----------------------|------|-------------|------|---------|----|--|--|---------------------|
| Dot defects | Scratches | lack dot hite dot on the polarizer of t | ≤0.1 | | Ignored | | | | Visual/ | | |
| bubble between the | | | 0.1< ∮ ≤0.20 | | 2 | J>5 | • | | contrast by Inspection standard | | |
| | dirty spot and | | 0.20< ∮≤0.25 | | 1 | J>10 | | | | | |
| | | | 0.25< ∮≤0.30 | | 0 | | | | film | | |
| | glass in the display area. | between dot and dot. | 0φ>0.3 | | 0 | | | | | | |
| black/white | Fibres in | K.W | W≤0.01 | | Ignored | | | | Visual/ | | |
| line defect (straight | active area, scratches | | W≤0.02 L≤5 | | 2 | J>5 | | | contrast by | | |
| line or | and black line on the | | W≤0.03 L≤4 | | 1 | J>10 | • | | Inspection standard | | |
| curve etc. Line type | glass or | L | W≤0.04 L≤3 | | 0 | J>10 | | | film | | |
| defects) | polarizer. | J:the distance between dot and dot. | W≤0.05 L≤2 | | 0 | | | | | | |
| | sidestep on the lower glass | | | T. | Y≤1/2L, X≤1 | | Ignored | | | | Visual/ contrast |
| | | Y:width of chip X:length of chip L:width of sidestep | Y≤1/2L, X≤2 | | 2 | | | | by Inspection standard film | | |
| | | | Y≤1/2L, X≤3 | | 1 | | _ | • | | | |
| | | | Y≤1/2L, X≤1/3 | J | 0 | J≤3 | | | | | |
| | | J:distance between electrode and the farthermost edge. | Y≤1/2L, X≤2/3 | J | 0 | J≤3 | | | | | |
| Crack | | | Y≤1/5L X≤5 ≤1/2T | Z | Ignored | | | | Visual/ contrast | | |
| | | | Y≤1/4L X≤5 ≤1/2T | Z | 2 | | | | by Inspection standard | | |
| | Y:width of crack X:length of crack L:width of sidestep T:deepth of crack Z:thickness of single glass | | Y≤1/3L X≤5 2 1/2T | Z≤ | 1 | | | | film | | |
| | | L:width of sidestep T:deepth of crack | Y≤1/3L X≤10 ≤1/2T | Z | 0 | | | | | | |
| | | | Y≤1/3L X≤15 ≤1/2T | Z | 0 | | | | | | |
| Crack | | | Cracks in any area | reje | cted | | • | | Visual | | |
| Polarizer | | | ≪0.8 | Acc | cepted | | | • | Visual/ | | |

| | be applicable for | 0.8 <l≤1.0< th=""><th>Rejected</th><th></th><th>contrast by</th></l≤1.0<> | Rejected | | contrast by |
|--------------------|---|---|--|---|---|
| | up/bottom polarizer | 1.0 <l≤1.5< td=""><td>Rejected</td><td></td><td>Inspection</td></l≤1.5<> | Rejected | | Inspection |
| | | 1.5 <l≤2.0< td=""><td>Rejected</td><td></td><td>standard film</td></l≤2.0<> | Rejected | | standard film |
| | | | Any seeable polarizer slanting or excursion in active area will be rejected. | | |
| | | ge should be even and ention within 1/3 of ill be rejected. | | | |
| | | Wrong direction, incorrect sticking surface(grease) rejected. | | | |
| | | seeable black sil | kscreen line from the cepted. | | |
| | | Refer to the draw | ving size requirement. | | |
| | | | | | |
| End seal | | UV glue of seal on the glass surface | Rejected | | Visual/ contrast by |
| | L:The distance from the block to edge of glass. | The UV glue of seal overflow into the active area. | Rejected | | Inspection standard film |
| | | Direction of end seal is different from the drawing. | Rejected | • | |
| | | Glue capacity of end seal < (1/3)*L | Rejected | | |
| | | the height and length of end seal is out of the drawing requirements. | Rejected | | |
| Silkscreen line | | silkscreen line overflow into the active area. | Rejected | | Visual/ contrast by Inspection |
| | | silkscreen line deviated in active area. | Rejected | • | standard film |
| | | bubble of silkscreen line ≥ 1/3 witdth of silkscreen line | Rejected | | |

| PIN | | | Glue on PIN: there is glue on the PIN without pin clip will be rejected. PIN glue solidification: PIN glue doesn't solidify completely. The sunken or glue stain by touching will be rejected. PIN deflection: if deflection angle >± 5°, rejected; contrarily, please refer to the drawing requirement. Without continuous glue on pins will be rejected. PIN glue stains on polarizer or inleakage polarizer and glass, rejected. PIN glue exceeds the up polarizer, rejected. Missing or extra, broken pin, rejected. PIN loosen: no permission for pin loose or drop. Clip PIN:pin center exceeds 1/3 ITO width, rejected. No pin glue, rejected. UV glue range: UV glue must be exceeded over 1~1.5 pin distance from both side. if not, rejected. PIN length and direction must be same with the drawing requirements. | | | | Visual/ contrast by Inspection standard film |
|--------------------|--|--|---|----------------------------|--|---|---|
| Protective film | | | LCD protective film can not stick on the polarizer and the product protective film raised ≤1/3 length or width of polarizer from same direction of axis and its total length should be ≤15mm. This defect can be accepted. | | | • | Visual |
| Rainbow | | | rainbow is not in active area. | rainbow is not in Accepted | | | Visual/co ntrast by |
| | | | Rainbow in active area. | Rejected | | • | golden sample |
| | | | with obvious discoloration and uneven color. | | | | |
| backgroud color | | | There are obvious different background color from the same product lot. | Rejected | | • | Visual/co ntrast by golden sample |

NOTE:

Inspection condition:

Viewing distance for cosmetic inspection is 30cm with bare eyes, and under an environment of 800 lux(20W*2---40W) light intensity, all directions for inspecting the sample should be within 45° against perpendicular line.

12.0 PRECAUTION FOR USING LCM

- 1. When design the product with this LCD Module, make sure the viewing angle matches to its purpose of usage.
- 2. As LCD panel is made of glass substrate, Dropping the LCD module or banging it against hard objects may cause cracking or fragmentation. Especially at corners and edges.
- 3. Although the polarizer of this LCD Module has the anti-glare coating, always be careful not to scratch its surface. Use of a plastic cover is recommended to protect the surface of polarizer.
- 4. If the LCD module is stored at below specified temperature, the LC material may freeze and be deteriorated. If it is stored at above specified temperature, the molecular orientation of the LC material may change to Liquid state and it may not revert to its original state. Excessive temperature and humidity could cause polarizer peel off or bubble. Therefore, the LCD module should always be stored within specified temperature range.
- 5. Saliva or water droplets must be wiped off immediately as those may leave stains or cause color changes if remained for a long time. Water vapor will cause corrosion of ITO electrodes.
- 6. If the surface of LCD panel needs to be cleaned, wipe it swiftly with cotton or other soft cloth. If it is not still clean enough, blow a breath on the surface and wipe again.
- 7. The module should be driven according to the specified ratings to avoid malfunction and permanent damage. Applying DC voltage cause a rapid deterioration of LC material. Make sure to apply alternating waveform by continuous application of the M signal. Especially the power ON/OFF sequence should be kept to avoid latch-up of driver LSIs and DC charge up to LCD panel.
- 8. Mechanical Considerations
 - a) LCM are assembled and adjusted with a high degree of precision. Avoid excessive shocks and do not make any alterations or modifications. The following should be noted.
 - b) Do not tamper in any way with the tabs on the metal frame.
 - c) Do not modify the PCB by drilling extra holes, changing its outline, moving its components or modifying its pattern.
 - d) Do not touch the elastomer connector; especially insert a backlight panel (for example, EL).
 - e) When mounting a LCM makes sure that the PCB is not under any stress such as bending or twisting. Elastomer contacts are very delicate and missing pixels could result from slight dislocation of any of the elements.
 - f) Avoid pressing on the metal bezel, otherwise the elastomer connector could be deformed and lose contact, resulting in missing pixels.
- 9. Static Electricity
 - a) Operator

Ware the electrostatics shielded clothes because human body may be statically charged if not ware shielded clothes. Never touch any of the conductive parts such as the LSI pads; the copper leads on the PCB and the interface terminals with any parts of the human body.

b) Equipment

There is a possibility that the static electricity is charged to the equipment, which has a function of peeling or friction action (ex: conveyer, soldering iron, working table). Earth the equipment through proper resistance (electrostatic earth: 1x10⁸ ohm).

Only properly grounded soldering irons should be used.

If an electric screwdriver is used, it should be well grounded and shielded from commutator sparks.

The normal static prevention measures should be observed for work clothes and working benches; for the latter conductive (rubber) mat is recommended.

c) Floor

Floor is the important part to drain static electricity, which is generated by operators or equipment.

There is a possibility that charged static electricity is not properly drained in case of insulating floor. Set the electrostatic earth (electrostatic earth: $1x10^8$ ohm).

d) Humidity

Proper humidity helps in reducing the chance of generating electrostatic charges. Humidity should be kept over 50%RH.

e) Transportation/storage

The storage materials also need to be anti-static treated because there is a possibility that the human body or storage materials such as containers may be statically charged by friction or peeling.

The modules should be kept in antistatic bags or other containers resistant to static for storage.

f) Soldering

Solder only to the I/O terminals. Use only soldering irons with proper grounding and no leakage.

Soldering temperature : 355 $^{\circ}$ C \pm 10 $^{\circ}$ C

Soldering time: 3 to 4 sec.

Use eutectic solder with resin flux fill.

If flux is used, the LCD surface should be covered to avoid flux spatters. Flux residue should be removed afterwards.

g) Others

The laminator (protective film) is attached on the surface of LCD panel to prevent it from scratches or stains. It should be peeled off slowly using static eliminator.

Static eliminator should also be installed to the workbench to prevent LCD module from static charge.

- 10. Operation
 - a) Driving voltage should be kept within specified range; excess voltage shortens display life.
 - b) Response time increases with decrease in temperature.
 - c) Display may turn black or dark blue at temperatures above its operational range; this is (however not pressing on the viewing area) may cause the segments to appear "fractured".
 - d) Mechanical disturbance during operation (such as pressing on the viewing area) may cause the segments to appear "fractured".
- 11. If any fluid leaks out of a damaged glass cell, wash off any human part that comes into contact with soap and water. The toxicity is extremely low but caution should be exercised at all the time.
- 12. Disassembling the LCD module can cause permanent damage and it should be strictly avoided.
- 13. LCD retains the display pattern when it is applied for long time (Image retention). To prevent image retention, do not apply the fixed pattern for a long time. Image retention is not a deterioration of LCD. It will be removed after display pattern is changed.
- 14. Do not use any materials, which emit gas from epoxy resin (hardener for amine) and silicone adhesive agent (dealcohol or deoxym) to prevent discoloration of polarizer due to gas.
- 15. Avoid the exposure of the module to the direct sunlight or strong ultraviolet light for a long time.