ZETTLER DISPLAYS

XIAMEN ZETTLER ELECTRONICS CO., LTD.

SPECIFICATIONS FOR LIQUID CRYSTAL DISPLAY

	CUSTOMER APP	PROVAL	
※ PART NO. : <u>A</u>	AQM1264EY-FLW-FBW		PLAYS) VER1.0
APPROVAL		COMPANY CHOP	
CUSTOMER			
COMMENTS			

ZETTLER DIS	ZETTLER DISPLAYS ENGINEERING APPROVAL										
DESIGN BY	DESIGN BY CHECKED BY APPROVED BY										
Jacky		GZH									

REVISION RECORD

REVISION	REVISION DATE	PAGE	CONTENTS
	RECORD REVISION DATE 2017-11-17	PAGE	FIRST ISSUE
L			

***** CONTENTS

- 1.0 GENERAL SPECS
- 2.0 ABSOLUTE MAXIMUM RATINGS
- 3.0 ELECTRICAL CHARACTERISTICS
- 4.0 OPTICAL CHARACTERISTICS
- 5.0 BLOCK DIAGRAM
- 6.0 PIN ASSIGNMENT
- 7.0 POWER SUPPLY
- 8.0 TIMING CHARACTERISTICS
- 9.0 MECHANICAL DRAWING
- 10.0 RELIABILITY TEST
- 11.0 DISPLAY INSTRUCTION TABLE
- 12.0 APPEARANCE CRITERIA
- 13.0 PRECAUTION FOR USING LCM

1.0 GENERAL SPECS

1. Display Format	128x64 dots
2. Power Supply	3.3V
3. Module dimension	71.5mm(L) x46.3mm(W) x 5.7mm MAX (T)
4. Active display area (A/A)	55.0mm(L) x 27.48mm(W)
5. View area (V/A)	59.0mm(L) x 30.5mm(W)
6. Dot Size	0.39mm(W) x 0.39mm(H)
7. Dot Pitch	0.43mm(W) x 0.43mm(H)
8. Driver method	1/65 duty,1/9 bias, Vop=9.0V
9. Display mode	Positive / Transflective
10. LCD type	FSTN (BLACK FONT ON BLUE BACKGROUND)
11. Driver IC	ST7565R COG
12. Backlight Options	LED (WHITE)
13. ROHS	ROHS compliant

2.0 ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Min	Тур	Max	Unit
Operating temperature	Тор	-20	-	70	°C
Storage temperature	Tst	-30	-	80	°C
Input voltage	Vin	Vss-0.3		Vdd+0.3	V
Supply voltage for logic	Vdd- Vss	-0.3	-	3.6V	V
DC Supply Voltage	(Vout)	6.0		13.5	V
Supply voltage for LCD drive	Vo	4.0		13.5	V

3.0 ELECTRICAL CHARACTERISTICS

3.1 Electrical Characteristics Of LCM

Item	Symbol	Condition	Min	Тур	Max	Unit		
Power Supply Voltage	Vdd	25°C	3.1	3.3	3.5	V		
Power Supply Current	ldd	Vdd=3.3V			2	mA		
Input voltage (high)	Vih	H level	0.8Vdd		Vdd	V		
Input voltage (low)	Vil	L level	0		0.2Vdd	V		
		-20°C						
Recommended LC Driving		25°C	8.8	9.0	9.2	V		
Voltage	Vo	70°C				1		

3.2 The Characteristics Of LED Backlight

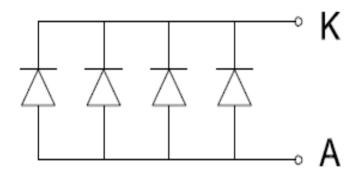
3.2.1 Electrical-Optical Characteristics Of LED Backlight (Ta=25°C)

Item	Symbol	Condition	Min	Тур	Max	Unit
Forward Current	Vf	IF=60mA	2.8	3.0	3.2	V
Reverse Voltage	Vr			5.0		V
Luminance	Lv	If=60mA	150			cd/m²
Uniformity	Δ	(Lvmin/Lvmax)%	70%	70%		
Dominan wave length	λР					nm
Chroma coordinate	х	If=60mA	0.26		0.30	um
Chioma coordinate	у	If=60mA	0.27		0.31	um
Lifetime		If=60mA		20000		Hours

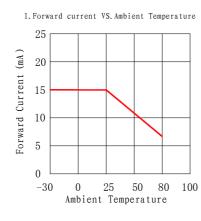
NOTE:

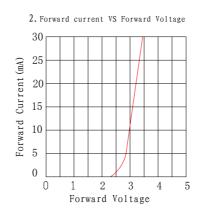
- (1) Forward voltage means voltage applied directly to the LED
- (2)The luminance is the average value of 5 points, The measurement instrument is BM-7 luminance colorimeter. The diameter of aperture is Φ 5mm
 - (3) Luminance means the backlight brightness without LCD.
- (4) Backlight lifetime means luminance value larger than half of the original after 20000 hours' continuous working.

3.2.2 Backlight Control Circuit FOR LCM (1x4=4PCS LED)



3.2.3 LED Characteristics Curves (for single led)

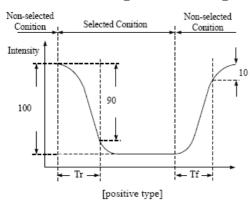


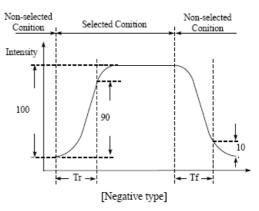


4.0 OPTICAL CHARACTERISTICS (Ta=25°C, Vdd= 3.3V±0.2V)

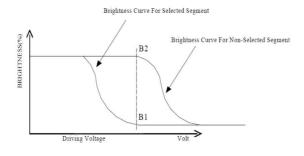
Item	Symbol	Condition	Min	Тур	Max	Unit
Viewing angle (horizontal)	θ	Cr ≥ 2.0	-35	-	35	deg
Viewing angle (vertical)	ф	Cr ≥ 2.0	-25	-	40	deg
Contrast Ratio	Cr	φ=0°, θ=0°	-	4	-	
Response time (rise)	Tr	φ=0°, θ=0°	-	180	300	ms
Response time (fall)	Tf	φ=0°, θ=0°	-	150	250	ms

(1). Definition of Optical Response Time



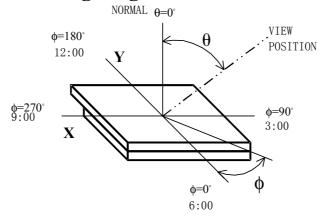


(2). Definition of Contrast Ratio

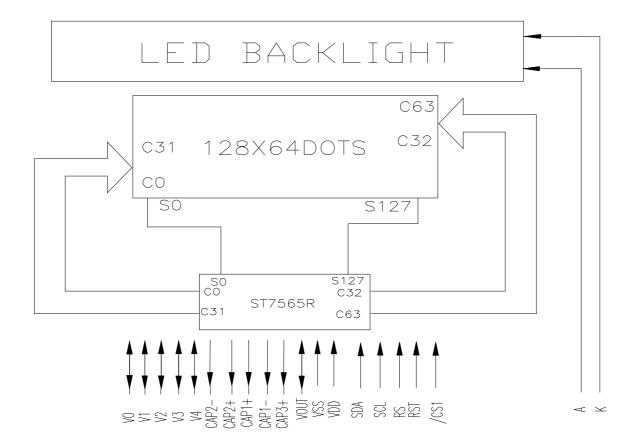


Cr= Brightness of Non-selected Segment(B2)
Brightness of selected Segment(B1)

(3). Definition of Viewing Angle θ and Φ



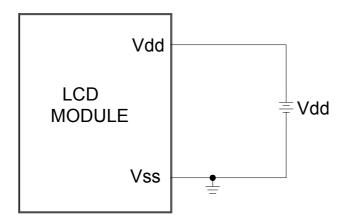
5.0 BLOCK DIAGRAM



6.0 PIN ASSIGNMENT

Pin No	Name	Description
1~5	V0~V4	LCD driver supplies voltages
6	CAP2-	Connect a capacitor between this terminal and the CAP2+ terminal
7	CAP2+	Connect a capacitor between this terminal and the CAP2- terminal
8	CAP1+	Connect a capacitor between this terminal and the CAP1- terminal
9	CAP1-	Connect a capacitor between this terminal and the CAP1+ terminal
10	CAP3+	Connect a capacitor between this terminal and the CAP1- terminal
11	VOUT	Connect a capacitor between this terminal and VSS or VDD terminal
12	VSS	Power supply(0V)
13	VDD	Power supply(+3.3)
14	SDA	Serial data input
15	SCL	Serial clock input
16	RS	It decide whether the data bits are data or a command.
10	K3	"L" is for command and "H" is for data.
17	RST	Chip reset signal. Active when low
18	/CS1	Chip select signal. Active when low

7.0 POWER SUPPLY



8.0 TIMING CHARACTERISTICS

The 4-line SPI Interface

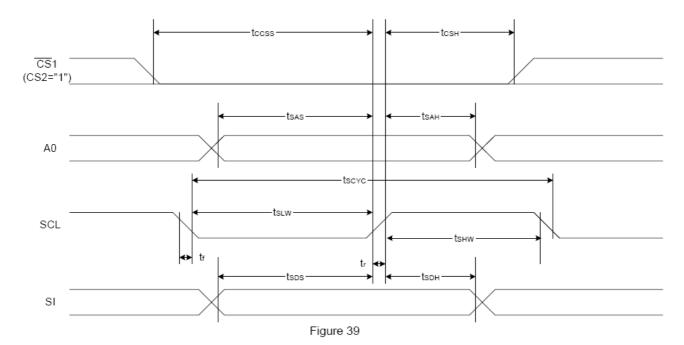


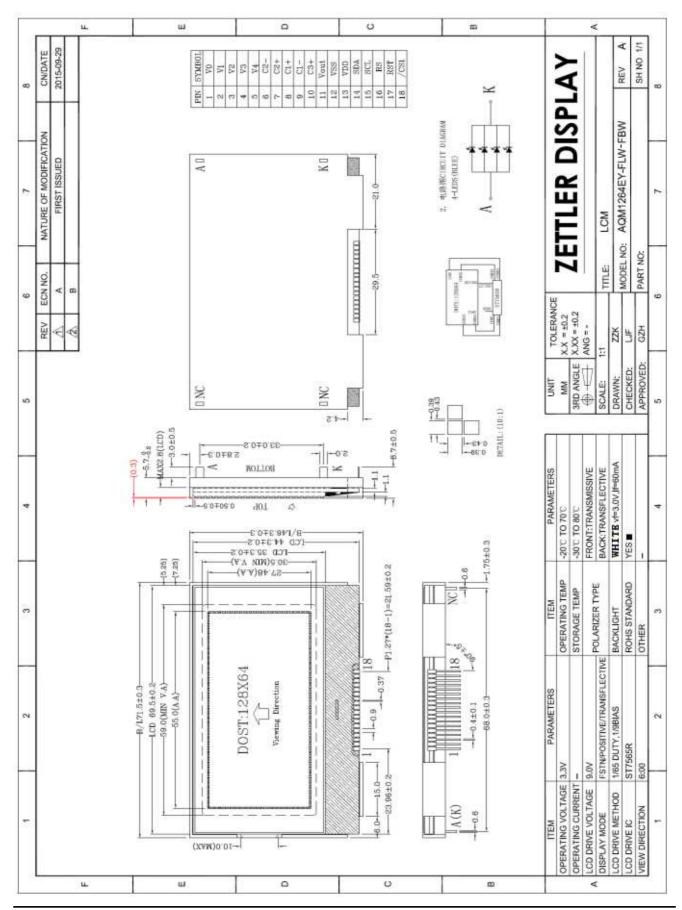
Table 28

				(VDD = 3.3V)	Ta = -30 to	85°C)
Item	Signal	Symbol	Condition	Rat	Units	
item	Sigilal	Symbol	Condition	Min.	Max.	Units
4-line SPI Clock Period		Tscyc		50	_	
SCL "H" pulse width	SCL	Tshw		25	_]
SCL "L" pulse width		Tslw		25	_]
Address setup time	A0	Tsas		20	_]
Address hold time	AU	Tsah		10	_	ns
Data setup time	- SI	Tsds		20	_]
Data hold time	31	TsdH		10	_]
CS-SCL time	cs	Tcss		20	_]
CS-SCL time		Tcsh		40	_	

NOTE:

- 1. The input signal rise and fall time (tr, tf) are specified at 15 ns or less.
- 2. All timing is specified using 20% and 80% of VDD as the standard.

9.0 MECHANICAL DRAWING



10.0 RELIABILITY TEST

NO	To	est Item	Description	Test Condition	Remark
1		High temperature storage	Applying the high storage temperature Under normal humidity for a long time Check normal performance	80 °C 96hrs	
2		Low temperature storage	Applying the low storage temperature Under normal humidity for a long time Check normal performance	-30°C 96hrs	
3		High temperature Operation	Apply the electric stress(Volatge and current) Under high temperature for a long time	70 °C 96hrs	Note1
4	Environmental	Low temperature Operation	Apply the electric stress Under low temperature for a long time	-20 ° C 96hrs	Note1 Note2
5	Test	High temperature/High Humidity Storage	Apply high temperature and high humidity storage for a long time	90% RH 40 ° C 96hrs	Note2
6		Temperature Cycle	Apply the low and high temperature cycle -30°C <> 25°C <> 80°C <> 25°C 30min 10min 30min 10min 1 cycle Check normal performance	-30°C/80°C 10 cycle	
7	Mechanical Test	Vibration test(Package state)	Applying vibration to product check normal performance	Freq:10-55Hz Max Acceleration 5G 1cycle time:1min time X.Y.Z direction for 15 mines	
8		Shock test(package state)	Applying shock to product check normal performance	Drop them through 70cm height to strike horizontal plane	
9	Other			•	

Remark

Note1:Normal operations condition (25°C±5°C).

Note2:Pay attention to keep dewdrops from the module during this test.

11.0 DISPLAY INSTRUCTION TABLE

					Com	mano	d Cod	le				
Command	Α0	/RD	/WR	D7	D6	D5	D4	D3	D2	D1	D0	Function
(1) Display ON/OFF	0	1	0	1	0	1	0	1	1	1	0 1	LCD display ON/OFF 0: OFF, 1: ON
(2) Display start line set	0	1	0	0	1		Disp	ay st	art a	ddres	s	Sets the display RAM display start line address
(3) Page address set	0	1	0	1	0	1	1	Р	age	addre	ess	Sets the display RAM page address
(4) Column address set upper bit Column address set lower bit	0	1	0	0	0	0	1	co Le	lumn ast s	ignific addr ignific addr	ess cant	Sets the most significant 4 bits of the display RAM column address. Sets the least significant 4 bits of the display RAM column address.
(5) Status read	0	0	1		Sta	itus		0	0	0	0	Reads the status data
(6) Display data write	1	1	0					Wı	rite d	ata		Writes to the display RAM
(7) Display data read	1	0	1					Re	ad d	ata		Reads from the display RAM
(8) ADC select	0	1	0	1	0	1	0	0	0	0	0	Sets the display RAM address SEG output correspondence 0: normal, 1: reverse
(9) Display normal/ reverse	0	1	0	1	0	1	0	0	1	1	0 1	Sets the LCD display normal/ reverse 0: normal, 1: reverse
(10) Display all points ON/OFF	0	1	0	1	0	1	0	0	1	0	0 1	Display all points 0: normal display 1: all points ON
(11) LCD bias set	0	1	0	1	0	1	0	0	0	1	0 1	Sets the LCD drive voltage bias ratio 0: 1/9 bias, 1: 1/7 bias (ST7565R)
(12) Read-modify-write	0	1	0	1	1	1	0	0	0	0	0	Column address increment At write: +1 At read: 0
(13) End	0	1	0	1	1	1	0	1	1	1	0	Clear read/modify/write
(14) Reset	0	1	0	1	1	1	0	0	0	1	0	Internal reset
(15) Common output mode select	0	1	0	1	1	0	0	0	*	*	*	Select COM output scan direction 0: normal direction 1: reverse direction
(16) Power control set	0	1	0	0	0	1	0	1	0	perat mode	_	Select internal power supply operating mode
(17) V ₀ voltage regulator internal resistor ratio set	0	1	0	0	0	1	0	0	Res	sistor	ratio	Select internal resistor ratio(Rb/Ra) mode
(18) Electronic volume mode set Electronic volume	0	1	0	1	0	0	0	0		0	1	Set the V ₀ output voltage electronic volume register
register set				0	0	E	lectro	onic \	/olun	ne val	lue	electionic volume register
(40) Olasan varida aat	_	_	•	1	0	1	0	1	1	0	0	0: Sleep mode, 1: Normal mode
(19) Sleep mode set	0	1	0	*	*	*	*	*	*	0	1 0	
(20) Booster ratio set	0	1	0	1	1	1	1	1	0	0	0	select booster ratio 00: 2x,3x,4x
(20) Dooster ratio set		_ '	J	0	0	0	0	0	0		p-up llue	01: 5x 11: 6x
(21) NOP	0	1	0	1	1	1	0	0	0	1	1	Command for non-operation
(22) Test	0	1	0	1	1	1	1	*	*	*	*	Command for IC test. Do not use this command

12. APPEARANCE CRITERIA

Item	Description	Picture	Specification			MA	MI	Inspection Method
Dot defects (black/whi te dot)	Scratches black dot white dot on the polarizer dirty spot and bubble between the polarizer and glass in the display area.		≤0.1 0.1< φ≤0.20	Ignored 2	J>5	•		Visual/ contrast by Inspection standard
			0.20< φ ≤ 0.25	1	J>10			
			0.25< ∮≤0.30	0				film
			0φ>0.3	0				
black/white line defect (straight	Fibres in active area, scratches and black line on the glass or polarizer.	L	W≤0.01	Ignored				Visual/
			W≤0.02 L≤5	2	J>5			contrast by
line or			W≤0.03 L≤4	1	J>10	- •		Inspection standard
curve etc. Line type defects)			W≤0.04 L≤3	0	J>10			film
		J:the distance between dot and dot.	W≤0.05 L≤2	0				
Chip on corner	sidestep on the lower glass	Y:width of chip X:length of chip L:width of sidestep J:distance between electrode and the farthermost edge.	Y≤1/2L, X≤1	Ignored		-	•	Visual/ contrast by Inspection standard film
			Y≤1/2L, X≤2	2				
			Y≤1/2L, X≤3	1				
			Y≤1/2L, X≤1/3J	0	J≤3			
			Y≤1/2L, X≤2/3J	0	J≤3			
Crack		Y:width of crack X:length of crack L:width of sidestep T:deepth of crack Z:thickness of single glass	Y≤1/5L X≤5 Z ≤1/2T	Ignored	Ignored			Visual/ contrast
			Y≤1/4L X≤5 Z ≤1/2T	2 2				by Inspection standard
			Y≤1/3L X≤5 Z≤ 1/2T	1			•	film
			Y≤1/3L X≤10 Z ≤1/2T	0				
			Y≤1/3L X≤15 Z ≤1/2T	0				
Crack			Cracks in any re area	jected		•		Visual
Polarizer			≤0.8 A	ccepted			•	Visual/

	be applicable for	0.8 <l≤1.0< th=""><th>Rejected</th><th></th><th>contrast</th></l≤1.0<>	Rejected		contrast
	up/bottom polarizer	1.0 <l≤1.5< td=""><td>Rejected</td><td>1 </td><td>by Inspection</td></l≤1.5<>	Rejected	1	by Inspection
		1.5 <l≤2.0< td=""><td>Rejected</td><td> </td><td>standard film</td></l≤2.0<>	Rejected		standard film
		Any seeable pola		1	
		excursion in active area will be rejected.			
			ge should be even and intion within 1/3 of ill be rejected.		
		Wrong direction, missing or extra, incorrect sticking for polarizer and dirty surface(grease) on polarizer will be rejected. seeable black silkscreen line from the arond can be accepted. Refer to the drawing size requirement.			
End seal		UV glue of seal on the glass surface	Rejected		Visual/ contrast by
	L:The distance from the block to edge of glass.	The UV glue of seal overflow into the active area.	Rejected		Inspection standard film
		Direction of end seal is different from the drawing.	Rejected		
		Glue capacity of end seal < (1/3)*L	Rejected		
		the height and length of end seal is out of the drawing requirements.	Rejected		
Silkscreen line		silkscreen line overflow into the active area.	Rejected		Visual/ contrast by Inspection
		silkscreen line deviated in active area.	Rejected		standard
		bubble of silkscreen line ≥ 1/3 witdth of silkscreen line	Rejected		

PIN		Glue on PIN: there is glue on the PIN without pin clip will be rejected. PIN glue solidification: PIN glue doesn't solidify completely. The sunken or glue stain by touching will be rejected. PIN deflection: if deflection angle > ± 5°, rejected; contrarily, please refer to the drawing requirement. Without continuous glue on pins will be rejected. PIN glue stains on polarizer or inleakage polarizer and glass, rejected. PIN glue exceeds the up polarizer, rejected. Missing or extra, broken pin, rejected. PIN loosen: no permission for pin loose or drop. Clip PIN:pin center exceeds 1/3 ITO width, rejected. No pin glue, rejected. No pin glue, rejected. 1~1.5 pin distance from both side. if not, rejected. PIN length and direction must be same with the drawing requirements.		•		Visual/ contrast by Inspection standard film
Protective film		LCD protective film can not stick on the polarizer and the product protective film raised \leq 1/3 length or width of polarizer from same direction of axis and its total length should be \leq 15mm . This defect can be accepted.			•	Visual
Rainbow		rainbow is not in active area.	Accepted			Visual/co ntrast by
		Rainbow in active area.	Rejected		•	golden sample
		with obvious discoloration and uneven color.	Rejected			
backgroud color		There are obvious different background color from the same product lot.	Rejected		•	Visual/co ntrast by golden sample

NOTE:

Inspection condition:

Viewing distance for cosmetic inspection is 30cm with bare eyes, and under an environment of 800 lux(20W*2---40W) light intensity, all directions for inspecting the sample should be within 45° against perpendicular line.

13.0 PRECAUTION FOR USING LCM

- 1. When design the product with this LCD Module, make sure the viewing angle matches to its purpose of usage.
- 2. As LCD panel is made of glass substrate, Dropping the LCD module or banging it against hard objects may cause cracking or fragmentation. Especially at corners and edges.
- 3. Although the polarizer of this LCD Module has the anti-glare coating, always be careful not to scratch its surface. Use of a plastic cover is recommended to protect the surface of polarizer.
- 4. If the LCD module is stored at below specified temperature, the LC material may freeze and be deteriorated. If it is stored at above specified temperature, the molecular orientation of the LC material may change to Liquid state and it may not revert to its original state. Excessive temperature and humidity could cause polarizer peel off or bubble. Therefore, the LCD module should always be stored within specified temperature range.
- 5. Saliva or water droplets must be wiped off immediately as those may leave stains or cause color changes if remained for a long time. Water vapor will cause corrosion of ITO electrodes.
- 6. If the surface of LCD panel needs to be cleaned, wipe it swiftly with cotton or other soft cloth. If it is not still clean enough, blow a breath on the surface and wipe again.
- 7. The module should be driven according to the specified ratings to avoid malfunction and permanent damage. Applying DC voltage cause a rapid deterioration of LC material. Make sure to apply alternating waveform by continuous application of the M signal. Especially the power ON/OFF sequence should be kept to avoid latch-up of driver LSIs and DC charge up to LCD panel.
- 8. Mechanical Considerations
 - a) LCM are assembled and adjusted with a high degree of precision. Avoid excessive shocks and do not make any alterations or modifications. The following should be noted.
 - b) Do not tamper in any way with the tabs on the metal frame.
 - Do not modify the PCB by drilling extra holes, changing its outline, moving its components or modifying its pattern.
 - d) Do not touch the elastomer connector; especially insert a backlight panel (for example, EL).
 - e) When mounting a LCM makes sure that the PCB is not under any stress such as bending or twisting. Elastomer contacts are very delicate and missing pixels could result from slight dislocation of any of the elements.
 - f) Avoid pressing on the metal bezel, otherwise the elastomer connector could be deformed and lose contact, resulting in missing pixels.
- 9. Static Electricity
 - a) Operator

Ware the electrostatics shielded clothes because human body may be statically charged if not ware shielded clothes. Never touch any of the conductive parts such as the LSI pads; the copper leads on the PCB and the interface terminals with any parts of the human body.

b) Equipment

There is a possibility that the static electricity is charged to the equipment, which has a function of peeling or friction action (ex: conveyer, soldering iron, working table). Earth the equipment through proper resistance (electrostatic earth: 1x10⁸ ohm).

Only properly grounded soldering irons should be used.

If an electric screwdriver is used, it should be well grounded and shielded from commutator sparks.

The normal static prevention measures should be observed for work clothes and working benches; for the latter conductive (rubber) mat is recommended.

c) Floor

Floor is the important part to drain static electricity, which is generated by operators or equipment.

There is a possibility that charged static electricity is not properly drained in case of insulating floor. Set the electrostatic earth (electrostatic earth: $1x10^8$ ohm).

d) Humidity

Proper humidity helps in reducing the chance of generating electrostatic charges. Humidity should be kept over 50%RH.

e) Transportation/storage

The storage materials also need to be anti-static treated because there is a possibility that the human body or storage materials such as containers may be statically charged by friction or peeling.

The modules should be kept in antistatic bags or other containers resistant to static for storage.

f) Soldering

Solder only to the I/O terminals. Use only soldering irons with proper grounding and no leakage.

Soldering temperature : 355 $^{\circ}$ C \pm 10 $^{\circ}$ C

Soldering time: 3 to 4 sec.

Use eutectic solder with resin flux fill.

If flux is used, the LCD surface should be covered to avoid flux spatters. Flux residue should be removed afterwards.

g) Others

The laminator (protective film) is attached on the surface of LCD panel to prevent it from scratches or stains. It should be peeled off slowly using static eliminator.

Static eliminator should also be installed to the workbench to prevent LCD module from static charge.

10. Operation

- a) Driving voltage should be kept within specified range; excess voltage shortens display life.
- b) Response time increases with decrease in temperature.
- c) Display may turn black or dark blue at temperatures above its operational range; this is (however not pressing on the viewing area) may cause the segments to appear "fractured".
- d) Mechanical disturbance during operation (such as pressing on the viewing area) may cause the segments to appear "fractured".
- 11. If any fluid leaks out of a damaged glass cell, wash off any human part that comes into contact with soap and water. The toxicity is extremely low but caution should be exercised at all the time.
- 12. Disassembling the LCD module can cause permanent damage and it should be strictly avoided.
- 13. LCD retains the display pattern when it is applied for long time (Image retention). To prevent image retention, do not apply the fixed pattern for a long time. Image retention is not a deterioration of LCD. It will be removed after display pattern is changed.
- 14. Do not use any materials, which emit gas from epoxy resin (hardener for amine) and silicone adhesive agent (dealcohol or deoxym) to prevent discoloration of polarizer due to gas.
- 15. Avoid the exposure of the module to the direct sunlight or strong ultraviolet light for a long time.