

AZ DISPLAYS, INC.

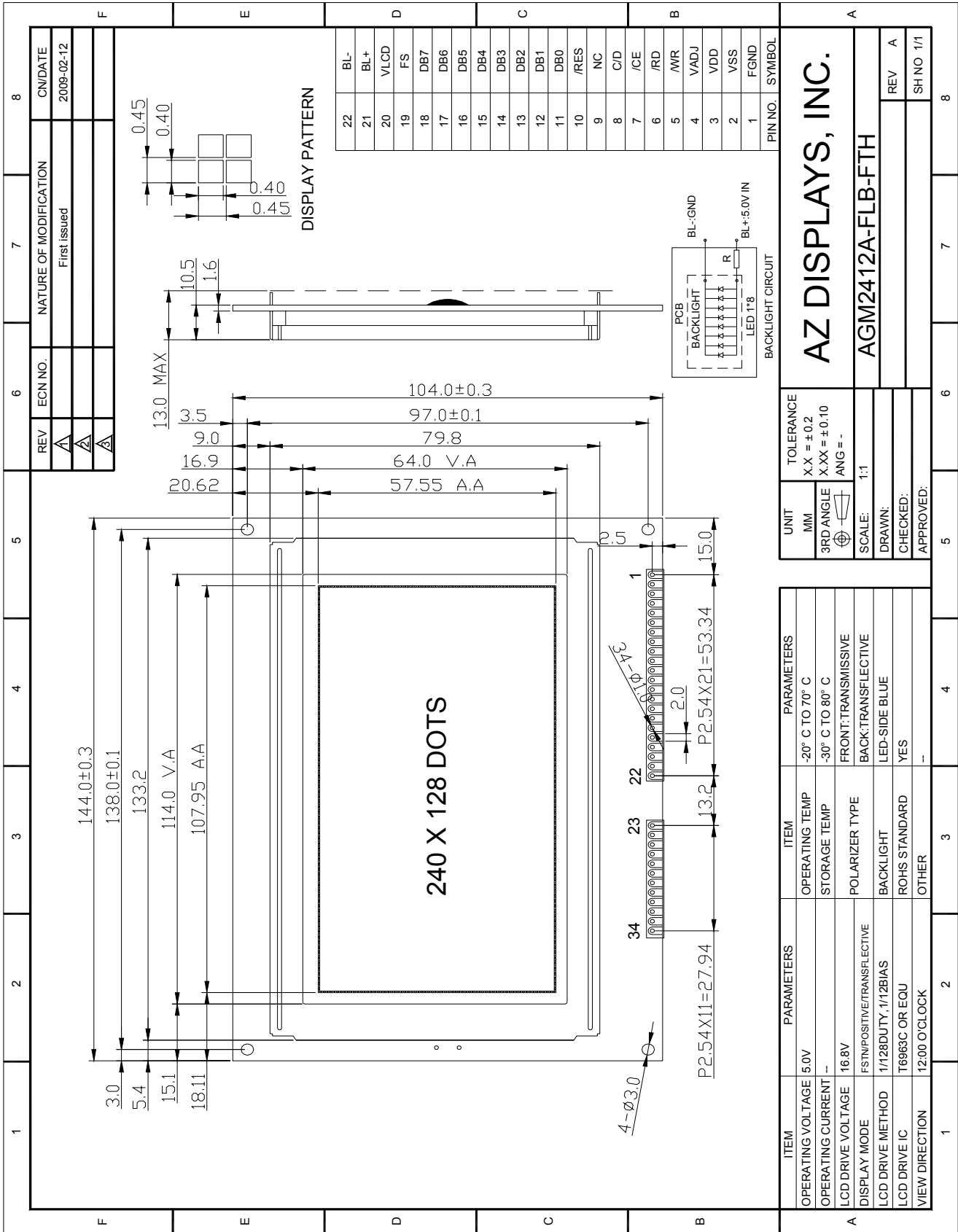
COMPLETE LCD SOLUTIONS

SPECIFICATIONS FOR LIQUID CRYSTAL DISPLAY

PART NUMBER:
DATE:

AGM2412A-FLB-FTH
MAY 26, 2009

1.0 MECHANICAL DIAGRAM



AGM2412A-FLB-FTH GRAPHIC MODULE VER1.0

2.0 MECHANICAL SPECS

1. Display Format	240*128 DOTS
2. Power Supply	5.0V
3. Overall Module Size	144.0mm(W) x 104.0mm(H) x max 13.0mm(D)
4. Viewing Area(W*H)	114.0mm(W) x 64.0mm(H)
5. Dot Size (W*H)	0.40mm(W) x 0.40mm(H)
6. Dot Pitch (W*H)	0.45mm(W) x 0.45mm(H)
7. Viewing Direction	12:00 O'Clock
8. Driving Method	1/128 Duty, 1/12 Bias
9. Controller IC	T6963C OR EQUIV
10. LC Fluid Options	FSTN /Positive
11. Polarizer Options	Transflective
12. Backlight Options	LED-SIDE(Blue)
13. Operating temperature	-20°C ~70°C
14. Storage temperature	-30°C ~ 80°C
15. ROHS	ROHS compliant

3.0 ABSOLUTE MAXIMUM RATINGS

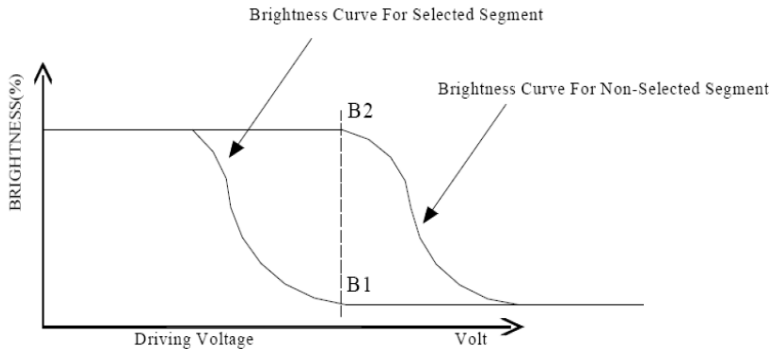
Item	Symbol		Min	Typ	Max	Unit
Operating temperature		Top	-20	-	70	°C
Storage temperature		Tst	-30	-	80	°C
Input voltage		Vin	-0.3	-	Vdd+0.3	V
Supply voltage for logic		Vdd- Vss	-0.3	-	7.0	V
Supply voltage for LCD drive		Vdd- V0	6		28	V

4.0 ELECTRICAL CHARACTERISTICS

4.1 Electrical Characteristic Of LCM (VDD=5.0V, Ta=25°C)

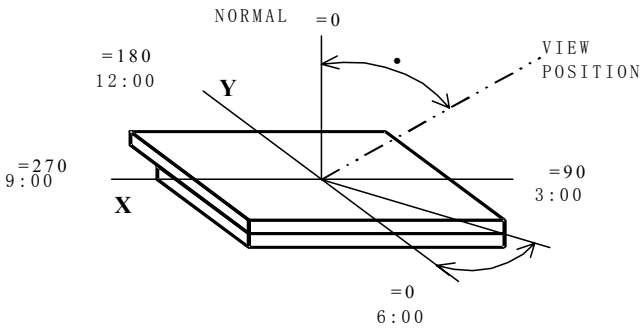
Item	Symbol	Condition	Min	Typ	Max	Unit
Power Supply Voltage	VDD	Ta=25°C		5.0		V
Power Supply Current	Idd	Vdd=5.0V	--	15	25	mA
Input voltage (high)	Vih	H level	VDD-2.2	--	VDD	V
Input voltage (low)	Vil	L level	0	--	0.8	V
Recommended LC Driving Voltage	Vdd -Vo	-20°C	--	--	--	V
		25°C	16 .4	16.8	17.2	
		70°C	--	--	--	

(2). Definition of Contrast Ratio

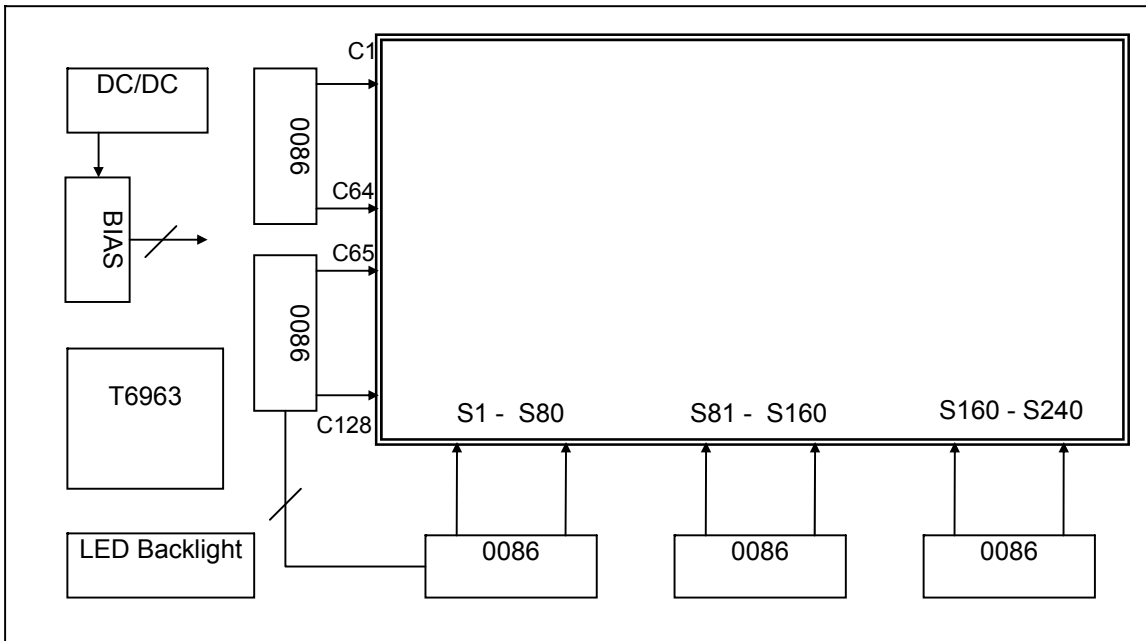


$$Cr = \frac{\text{Brightness of Non-selected Segment(B2)}}{\text{Brightness of selected Segment(B1)}}$$

(3). Definition of Viewing Angle and



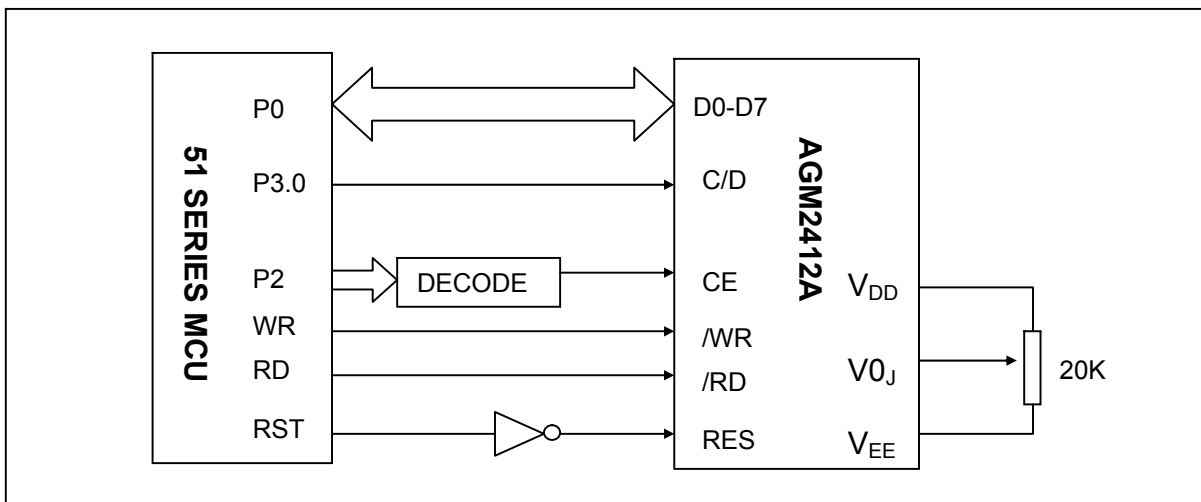
6.0 BLOCK DIAGRAM



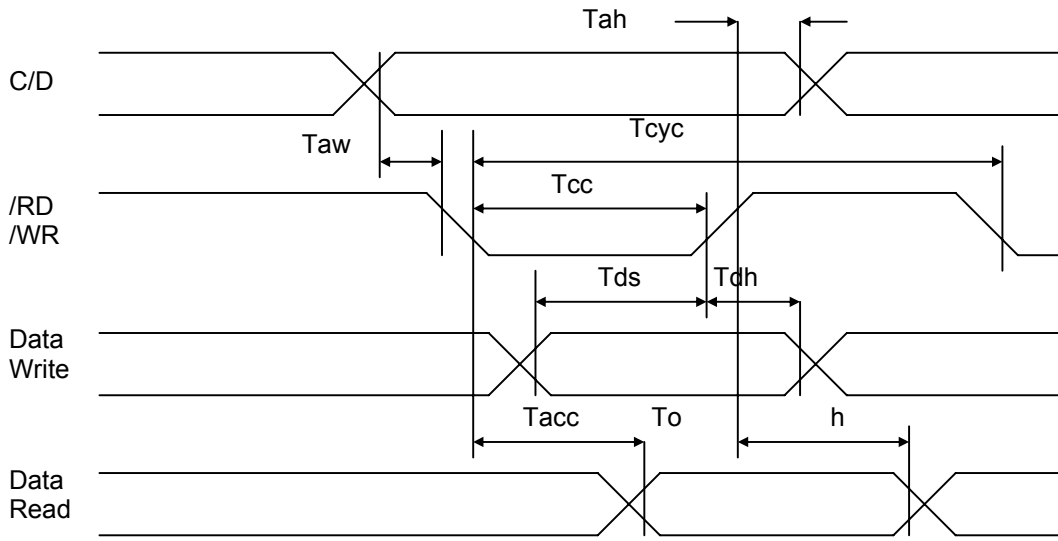
7.0 PIN ASSIGNMENT

Pin No.	Symbol	Function	Level
1	FGND	BEZEL Ground	-
2	VSS	Ground	
3	VDD	+5V -	
4	V _{adj}	Contrast Adjust Voltage	-
5	/W	R	Data write
6	/	RD	Data read
7	/	CE	Chip Enable
8	C	/D	Command/Data
9	N	C	
10	/	RES	Reset
11		DB0	Data bit 0
12		DB1	Data bit 1
13		DB2	Data bit 2
14		DB3	Data bit 3
15		DB4	Data bit 4
16		DB5	Data bit 5
17		DB6	Data bit 6
18		DB7	Data bit 7
19		FS	Font Select
			H=6*8dots matrix, L=8*8 dot matrix
20	VLCD	Power supply for LCD driving	-14.0V
21	BL+	Power supply for LED+	5.0V
22	BL-	Power supply for LED-	0V

8.0 APPLICATION

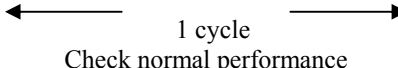


9.0 TIMING CHARACTERISTICS



ITEM	SYMBLE	MIN	MAX	UNIT
Address Setup Time	Taw	10		ns
Address Hold Time	Tah	10		ns
RD#,WR# Cycle	Tcyc	200		ns
RD#,WR# Pulse Width	Tcc	80		ns
Data Setup Time	Tds	80		ns
Data Hold Time (Write)	Tdh	40		ns
Data Access Time	Tacc		150	ns
Data Hold Time (Read)	Toh	10	50	ns

10.0 RELIABILITY TEST

NO	Test Item	Description	Test Condition	Remark	
1	Environmental Test	High temperature storage	Applying the high storage temperature Under normal humidity for a long time Check normal performance	80 °C 96hrs	
2		Low temperature storage	Applying the low storage temperature Under normal humidity for a long time Check normal performance	-30°C 96hrs	
3		High temperature Operation	Apply the electric stress(Voltage and current) Under high temperature for a long time	70 °C 96hrs	Note1
4		Low temperature Operation	Apply the electric stress Under low temperature for a long time	-20°C 96hrs	Note1 Note2
5		High temperature/High Humidity Storage	Apply high temperature and high humidity storage for a long time	90% RH 40°C 96hrs	Note2
6		Temperature Cycle	Apply the low and high temperature cycle -30°C <> 25°C <> 80°C <> 25°C 30min 10min 30min 10min  1 cycle Check normal performance	-30°C/80°C 10 cycle	
7	Mechanical Test	Vibration test(Package state)	Applying vibration to product check normal performance	Freq:10-55Hz Max Acceleration 5G 1cycle time:1min time X.Y.Z direction for 15 mins	
8		Shock test(package state)	Applying shock to product check normal performance	Drop them through 70cm height to strike horizontal plane	
9	Other				

Remark

Note1:Normal operations condition (25°C±5°C).

Note2:Pay attention to keep dewdrops from the module during this test.

11.0 DISPLAY CONTROL INSTRUCTION

The display control instructions control the internal state of the T6963c.

COMMAND	Control State			Code								DESCRIPTION
	CD	RD	WR	D7	D6	D5	D4	D3	D2	D1	D0	
REGISTERS SETTIBG	1	1	0	0	0	1	0	0	N2	N1	N0	N2 N1 N0 0 0 1 Set Cursor Pointer 0 1 0 Set Offset Register 1 0 0 Set address Pointer
SET CONTROL WORD	1	1	0	0	1	0	0	0	0	N1	N0	N1 N0 0 0 Set Text Home Address 1 1 Set Text Area 2 0 Set Graphic Home Address 1 1 Set Graphic Area
MODE SET	1	1	0	1	0	0	0	C G	N2	N1	N0	CG=0: Internal CG ROM Mode CG=1: Internal CG ROM Mode N2 N1 N0 Graphic & Text 0 0 0 OR Mode 0 0 1 Exor Mode 0 1 1 And Mode 1 0 0 Text Attribute Mode
DISPLAY MODE	1	1	0	1	0	0	1	N3	N2	N1	N0	N3=0: Display Off N3=1: Display On N2=0:Text Off N2=1: Text On N1=0: Cursor Off N1=1: Cursor On N0=0: Blink Off N0=1: Blink On
CURSOR PATTERN SELECT	1	1	0	1	0	1	0	0	N2	N1	N0	N2,N1,N0 Line Number of Cursor N2 N1 N0 0 0 0 1-Line Cursor (Bottom Line) 1 1 1 8-Line Cursor (8x8Dots)
DATA AUTO READ/WRITE	1	1	0	1	0	1	1	0	0	N1	N0	N1 N0 0 0 Data Auto Write 0 1 Data Auto Read 1 * Auto Reset
DATA READ/WRITE	1	1	0	1	1	0	0	0	N2	N1	N0	N2=0: ADP Variable N2=1: ADP Nonvariable N1=0: Increment ADP N1 =1: Decrement ADP N0=0: Data Write N0=1: Data Read
SCREEN PEEK	1	1	0	1	1	1	0	0	0	0	0	Screen Peek
SCREEN COPY	1	1	0	1	1	1	0	1	0	0	0	Screen Copy
BIT SET/RESET	1	1	0	1	1	1	1	N3	N2	N1	N0	N3=0: Bit Reset N3=1: Bit Set N2, N1, N0 Bit Address 000-D0, 111-D7
DATA WRITE	0	1	0	Write Data								Writes data DB0~DB7 into display data RAM. After writing instruction
DATA READ	0	0	1	Read Data								Reads data DB0~DB7 from display data RAM to the data bus.