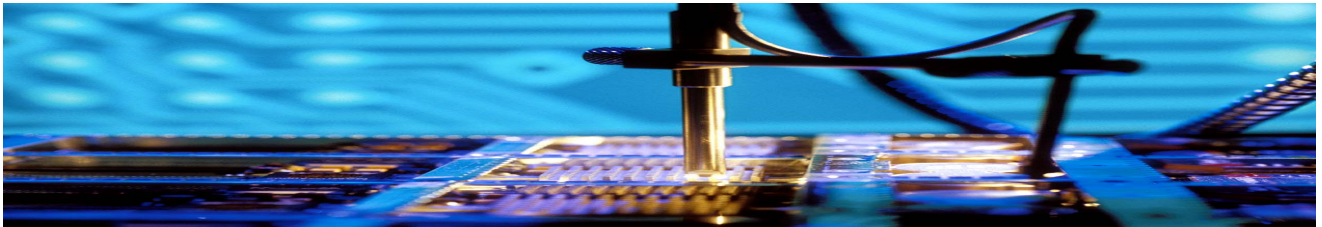


## TECHNICAL BRIEF

BY: AZD ENGINEERING

### TFT IC Chips

AZ Displays announces the availability of unique “combo ICs” for TFT displays which contain both NTSC decoder and timing (T-CON) IC features. Intended for use mainly in compact applications and small TFT panels like 2.5” and 3.5”, the following chips are available



### PVI-1005A

The PVI-1005A timing controller IC with C-C-Sync input is for high-resolution analog PVI TFT LCD modules. It is designed to support built-in horizontal frequency driver and phase comparator circuit for PLL circuit on internal clocks. This advantage allows the PVI-1005A to demonstrate its versatility to the customer's proprietary design without compromising functionality and performance. It supports PVI 16:9 aspect ratio TFT LCD modules as well as a range of display modes.



### PVI-5002A

The PVI-5002A is a one-chip solution with timing controller, C-Sync input and video decoder functions. This dual feature is intended for use in compact applications and with small TFT panels such as the 2.5”. This advantage allows the PVI-5002A to demonstrate its full adaptability to the customer's proprietary design without compromising functionality and performance. It also allows for easy model upgrades without design changes. Its versatile design enables a digital board to drive an analog panel, thus filling the nominal gap between the analog and digital domain.

