

TECHNICAL BRIEF

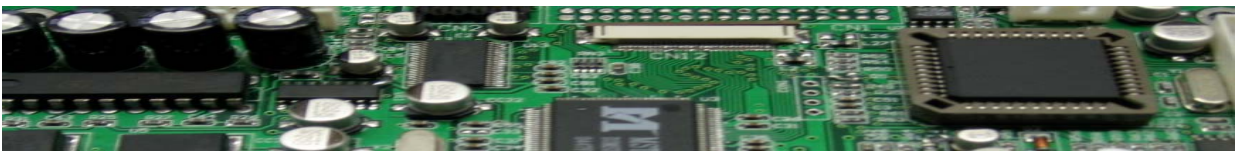
BY: AZD ENGINEERING

TFT IC Chips

AZ Displays announces the availability of unique “combo ICs” for TFT displays which contain both NTSC decoder and timing (T-CON) IC features. Intended for use mainly in compact applications and small TFT panels like 2.5” and 3.5”, the following chips are available

PVI-1004D for C-Sync Input

PVI-1004D is a one-chip solution with timing controller (TCON) and NTSC video decoder functions for Analog AZ Displays TFT LCD modules. It is designed to support both external clock, as well as built-in horizontal frequency driver and phase comparator circuit for PLL circuit on internal clocks. This advantage allows the PVI-1004D to demonstrate its versatility without compromising functionality and performance. It supports PVI 16:9 aspect ratio TFT LCD modules as well as a range of display modes.



PVI-5001A for Composite Input

The PVI-5001A is a one-chip solution with timing controller (T-CON) and video decoder functions. This dual feature is intended for use in compact applications and with small TFT panels such as the 2.5”. This advantage allows the PVI-5001A to demonstrate its full adaptability to the customer’s proprietary design without compromising functionality and performance. It also allows for easy model upgrades without design changes. Its versatile design enables a digital board to drive an analog panel, thus filling the nominal gap between the analog and digital domain. Customers can opt to upgrade panel sizes while sticking to the same system design. This chip is a low cost solution to system upgrades. The PVI-5001A does not require re-engineering to fit in different applications. This means the customer can use the same chip on new generation systems without investing in another IC timing chip. The chip supports PVI 16:9 aspect ratio TFT LCD modules as well as a range of display modes.

